

Type	Hits	Search Text	DBs	Time Stamp
120 BRS	305	database same design and abstraction and (integrated adj circuit)	USPAT; US-PGPUB; DERWENT	2003/03/20 14:10
121 BRS	179	database same design and abstraction and (integrated adj circuit) and schematic	USPAT; US-PGPUB; DERWENT	2003/03/20 14:11
122 BRS	53	database same design and abstraction and (integrated adj circuit) and schematic and verilog	USPAT; US-PGPUB; DERWENT	2003/03/20 14:20
123 BRS	19632	database same design and abstraction and (integrated adj circuit) and schematic and verlog ans "C++"	USPAT; US-PGPUB; DERWENT	2003/03/20 14:20
124 BRS	0	database same design and abstraction and (integrated adj circuit) and schematic and verilog and "C++"	USPAT; US-PGPUB; DERWENT	2003/03/20 14:21
125 BRS	9	database same design and abstraction and (integrated adj circuit) and schematic and verilog and "C++"	USPAT; US-PGPUB; DERWENT	2003/03/20 15:45
126 BRS	0	database same design adj capture and abstraction and (integrated adj circuit) and schematic and verilog and "C++"	USPAT; US-PGPUB; DERWENT	2003/03/20 15:48
127 BRS	0	database same design adj capture and abstraction and (integrated adj circuit) and schematic	USPAT; US-PGPUB; DERWENT	2003/03/20 15:50
128 BRS	1	database same design adj capture and abstraction and (integrated adj circuit) and schematic and verlog and "C++"	USPAT; US-PGPUB; DERWENT	2003/03/20 15:49
129 BRS	11	database same design adj capture and abstraction and (integrated adj circuit) and schematic	USPAT; US-PGPUB; DERWENT	2003/03/20 16:02
130 BRS	13	database same design adj capture and (integrated adj circuit) and schematic	USPAT; US-PGPUB; DERWENT	2003/03/20 16:05
131 BRS	1002	(logic or circuit) adj design and schematic and database	USPAT; US-PGPUB; DERWENT	2003/03/20 16:22
132 BRS	517	(logic or circuit) adj design and schematic and database and entry	USPAT; US-PGPUB; DERWENT	2003/03/20 16:32
133 BRS	136	(logic or circuit) adj design and schematic and database and vhdl and verilog	USPAT; US-PGPUB; DERWENT	2003/03/20 16:23
134 BRS	84	(logic or circuit) adj design and schematic and database and entry and vhdl and verilog	USPAT; US-PGPUB; DERWENT	2003/03/20 16:27
135 BRS	334	(logic or circuit) adj design and schematic and database and entry and rules	USPAT; US-PGPUB; DERWENT	2003/03/21 09:04